

PTO/SB/08a (08-03)



PTO/SB/08b(08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 2 of 2

Complete if Known

Application Number	10/805,822
Filing Date	March 22, 2004
First Named Inventor	Makoto YAMAMOTO
Art Unit	2815
Examiner Name	Tom Thomas
Attorney Docket Number	44471/298721

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
ET	4	MICHAEL P. MASQUELIER and DAVID N. OKADA, Institute of Electrical and Electronics Engineers, "Integration of a 200V, 60MHz Lateral PNP Transistor with Emitter-Base Self-Aligned to Polysilicon, into a High Voltage BiCMOS Process", Proceedings of the 3 rd International Symposium on Power Semiconductor Devices and ICs, pp. 56-60, XP000218972, 1991 IEEE, April 22-24, 1991, Baltimore, Maryland, USA	

Examiner
Signature

ET

Date
Considered

2-15-05

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Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	Not Yet Assigned 10/805822
		Filing Date	March 22, 2004
		First Named Inventor	Makoto Yamamoto
		Art Unit	2814
		Examiner Name	Rao, Shrinivas H.
		Attorney Docket Number	44471/298721
Sheet	1	of	1

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
EOL	1	US- 4,196,440	4-1-1980	Anantha et al.	
	2	US- 4,326,212	4-20-1982	Bergeron et al.	
	3	US- 5,665,994	9-9-1997	Palara	
	4	US- 5,708,290	1-13-1998	Cacciola et al.	
	5	US- 6,242,793	6-5-2001	Colombo et al.	
	6	US- 6,033,947	3-7-2000	Cacciola et al.	
	7	US- 6,225,181	5-1-2001	Gregory	
EOL	8	US- 4,149,906	4-17-1979	De La Moneda	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
EM	9	EP 0 545 488 A1	6-9-93	SGS-Thomson Microelectronics s.r.l.		

Examiner Signature		Date Considered	2-15-05
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